

9001 converter board pin assignment

Converter Board Pin connection				DIR9001 pin	DIR9001 pin Des
Converter BD pin	8414 Description	Connect to 9001 pin	9001 pin Description		
1	C	-		1	AUDIO
2	CD/F1	-		2	FSOUT0
3	CC/F0	17	EMPH	3	FSOUT1
4	CB/E2	-		4	SCKO
5	CA/E1	8	XTI	5	VDD
6	CD/E0	7	XTO	6	DGND
7	VD+	-		7	XTO
8	DGND	-		8	XTI
9	RXP	-		9	CLKST
10	RXN	-		10	LRCKO
11	FSYNC	10	LRCKO	11	BCKO
12	SCK	11	BCKO	12	DOUT
13	CS12	-		13	PSCK0
14	U	-		14	PSCK1
15	CBL	-		15	COUT
16	FCKSEL	-		16	UOUT
17	M3	-		17	EMPH
18	M2	-		18	FRAME
19	MCK	4	SCKO	19	RSV
20	FILT	-		20	RXIV
21	AGND	23	AGND	21	RST
22	VA+	-		22	FILT
23	M0	-		23	AGND
24	M1	-		24	VCC
25	ERF	27	ERROR	25	FMT0
26	SDATA	12	DOUT	26	FMT1
27	CE/F2	-		27	ERRPR
28	VERF	-		28	CKSEL

JUMPER SELECT

	9001 PIN	STATUS	MODE
J5	27 & 28	OPEN	PLL MODE
J5	27 & 28	SHORT	AUTO

	9001 PIN	STATUS	SETTING
J1	FMT0	SHORT	H
J2	FMT1	OPEN	L

Table 7. Serial Audio Data Output Format Set by FMT[1:0]

FMT[1:0] SETTINGS		DOUT SERIAL AUDIO DATA OUTPUT FORMAT
FMT1	FMT0	
L	L	16-bit, MSB-first, right-justified
L	H	24-bit, MSB-first, right-justified
H	L	24-bit MSB-first, left-justified
H	H	24-bit, MSB-first, I ² S

	9001 PIN	STATUS	SETTING
J3	PSCK0	SHORT	H
J4	PSCK1	OPEN	L

Table 3. SCKO, BCKO, and LRCKO Frequencies Set by PSCK[1:0]

PSCK[1:0] SETTING		OUTPUT CLOCK FROM PLL SOURCE		
PSCK1	PSCK0	SCKO	BCKO	LRCKO
L	L	128 f _s	64 f _s	f _s
L	H	256 f _s	64 f _s	f _s
H	L	384 f _s	64 f _s	f _s
H	H	512 f _s	64 f _s	f _s

Important Note: The pin 3 on DIR9001 converter board for EMPH is a reverse logic of that in CS8412. When High = data has pre-emphasis data added. While in CS8412, Low = data has pre-emphasis data added.

Example: **SM5842/SM5847/PMD100**

J1 open
 J2 open
 J3 short
 J4 open
 J5 open or short

TDA1541

J1 short
 J2 short
 J3 short
 J4 open
 J5 open or short

