

Pins	SM5842		PMD100		Remark
1	DI/INF2N	same	same	Din	
2	BCKI	same	same	BCKI	
3	CKSLN	same	L	XTIM	
4	INFIN	NC	to pin 21	DITH	Dither Sel: H = enable
5	IW1N/DIL	NC	output to pad	GAIN	output H when SCAL is L, else rev.
6	XTI	same	same	XTI	
7	XTO	NC	+5V	VDD1	
8	VSS	same	GND	VSS1	
9	CKO	NC	L	PROG	Standalone
10	IW2N/DIR	NC	L	OSIZ0	20 bit output mode
11	OW1N	NC	H	OSIZ1	
12	OW2N	same	H	COB-bar	
13	SYNCN	same	H	JUST	Right justified 16 bit input
14	RSTN	same	L	BCPL	latch on rising edge
15	DEMPR	NC	L	SMUTE	off
16	DEMPH	same	same	DEMPH	
17	FSEL1	NC	to pin 19	HMUTE	
18	FSEL2	same	L	FSEL	
19	MUTER	from pin 17	L/H	SCAL	H will give 0dB, L will -6dB on CD
20	MUTEL	NC	NC	DG	
21	DITHN	from pin 4	GND	VSS2	
22	VDD	same	+5V	VDD2	
23	DOR	same	same	DOR	
24	DOL	same	same	DOL	
25	WCKO	same	same	WCKO	
26	BCKO	same	same	BCKO	
27	DG	NC	to LED	HDCD	150R to LED to GND + pad
28	LRCI	same	same	LRCI	