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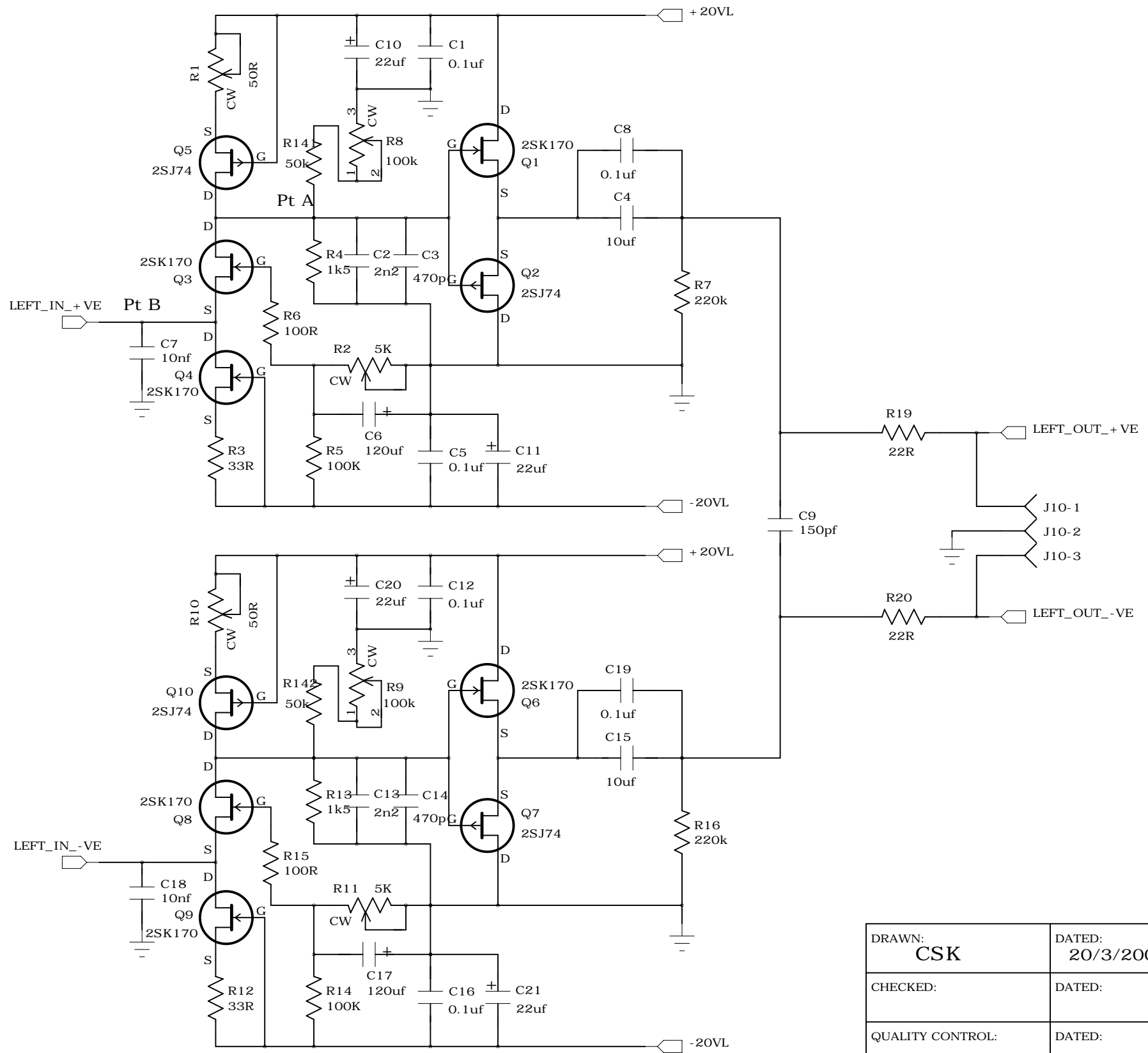
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Notes:

- Q1, Q2, Q6, & Q7 use Idss 7-9 mA, BL grade
- Q3 & Q8 use Idss > 10mA or V grade, or 2SK389V
- Q5 & Q10 use Idss > 15mA, V grade; or parallel 2 BL grade
- Q4 & Q9 use Idss 6-7 mA, BL grade
- Matched all Idss for best performance!
- R8 & R9 to trim the Jfet IV output to same level
- Use 2700pf to replace C2 & C3 and C13 & C14
- R1 and R10 can use 50 to 100 ohm 20T pot
- R1 to adjust Pt A to 10Vdc
- R2 to adjust Pt B to 0Vdc

D

D

C

C

B

B

A

A

COMPANY: <b>DIY FUN</b>			
TITLE: <b>Jfet IV Left Channel</b>			
CODE:	SIZE: <b>B</b>	DRAWING NO:	REV: <b>1</b>
SCALE: <b>Total 8 pages</b>		SHEET: OF <b>1</b>	

DRAWN: <b>CSK</b>	DATED: <b>20/3/2007</b>
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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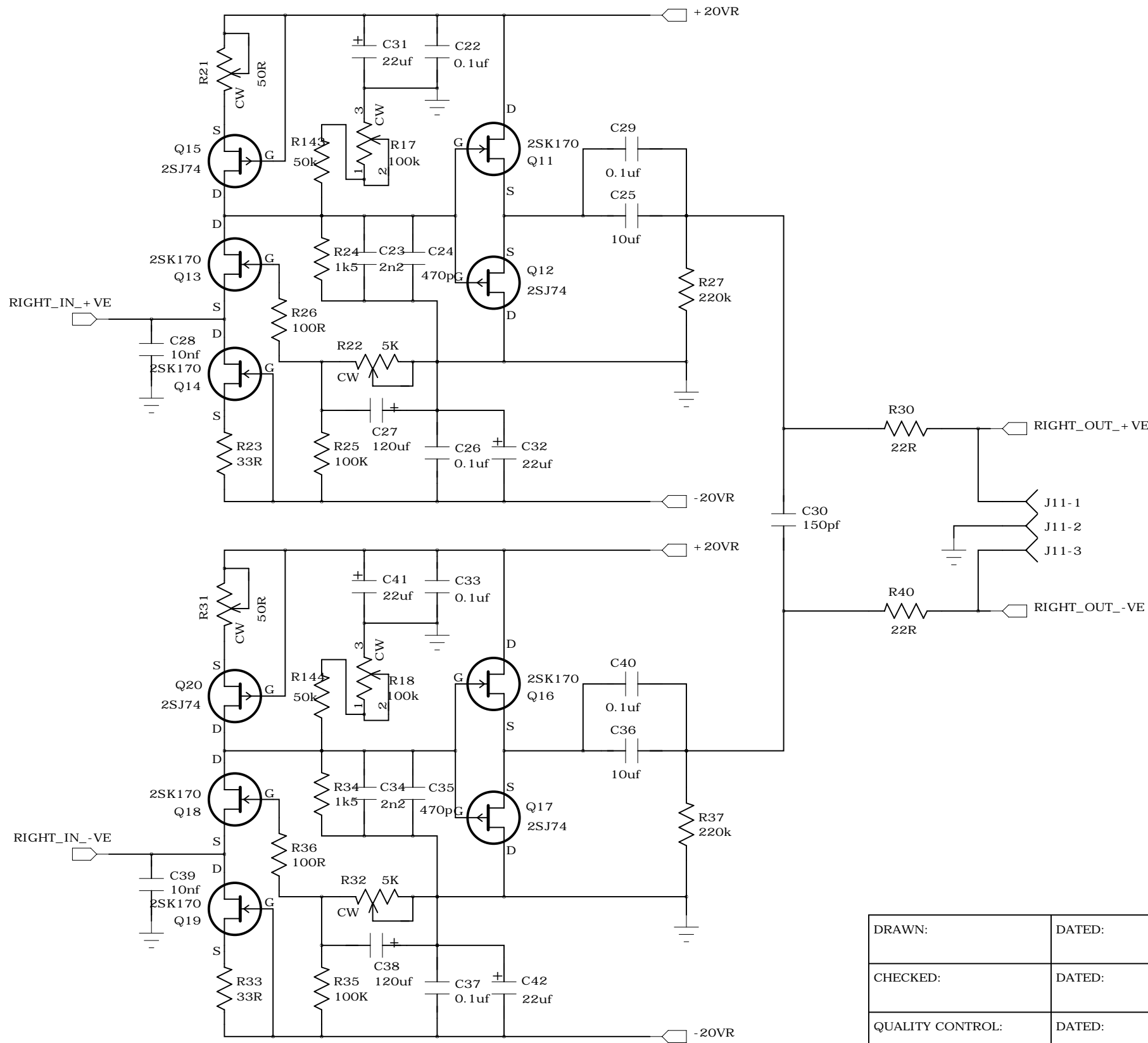
B

B

A

A

Same notes as in Left Channel Jfet IV



COMPANY:			
TITLE: <b>Jfet IV Right Channel</b>			
CODE:	SIZE: <b>B</b>	DRAWING NO:	REV: <b>A</b>
SCALE:		SHEET: OF <b>2</b>	

DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

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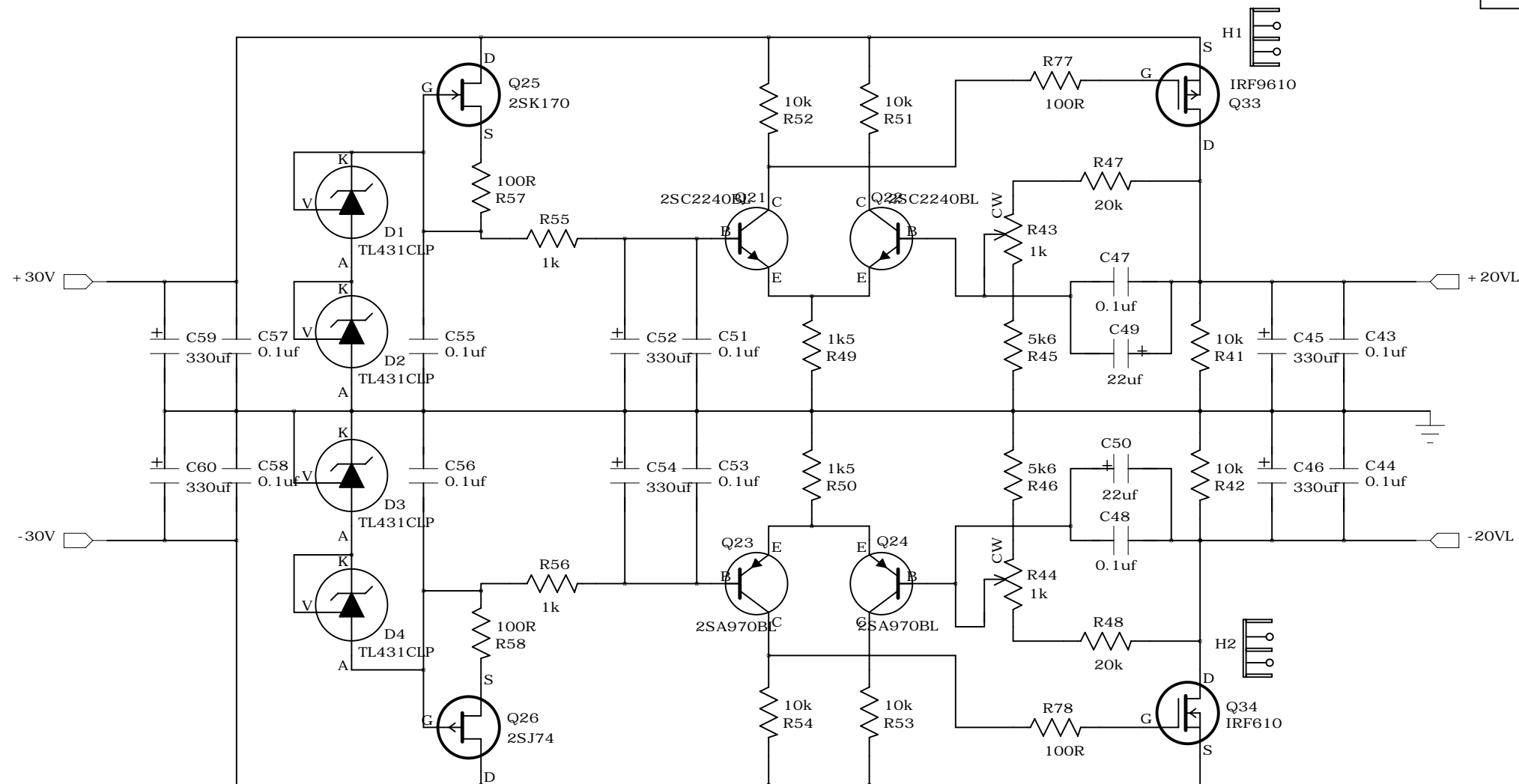
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Notes:

Q25 & Q26 use Idss 6-7 mA

D1 & D2 and D3 & D4 is 5V voltage reference, or LM336-5V one pc

Q21 & Q22 hfe > 350, or use BC550C

Q23 & Q24 hfe > 350, or use BC557C

Q33 and Q34 add small heat sink

Q25, Q26 and its source resistor can be replaced by 2mA current diode J508

R43 & R44 to adjust regulator output volt to +/-20Vdc

COMPANY:

TITLE:

REGULATOR LEFT JFET IV

DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
SCALE:			SHEET: OF 3

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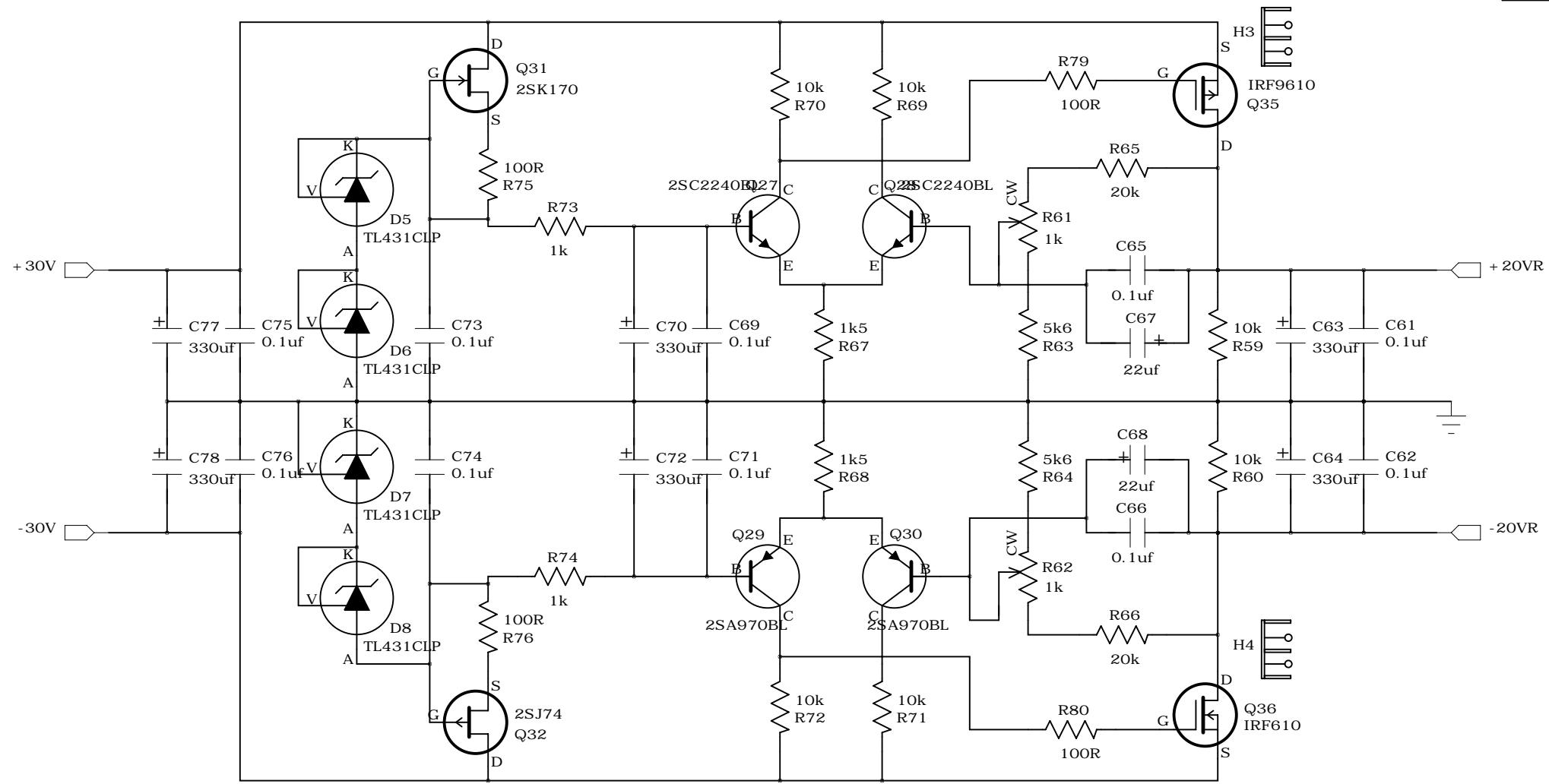
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Same notes as in Left Channel Regulator

COMPANY:

TITLE:

REGULATOR RIGHT JFET IV

DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
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SCALE: SHEET: OF 4

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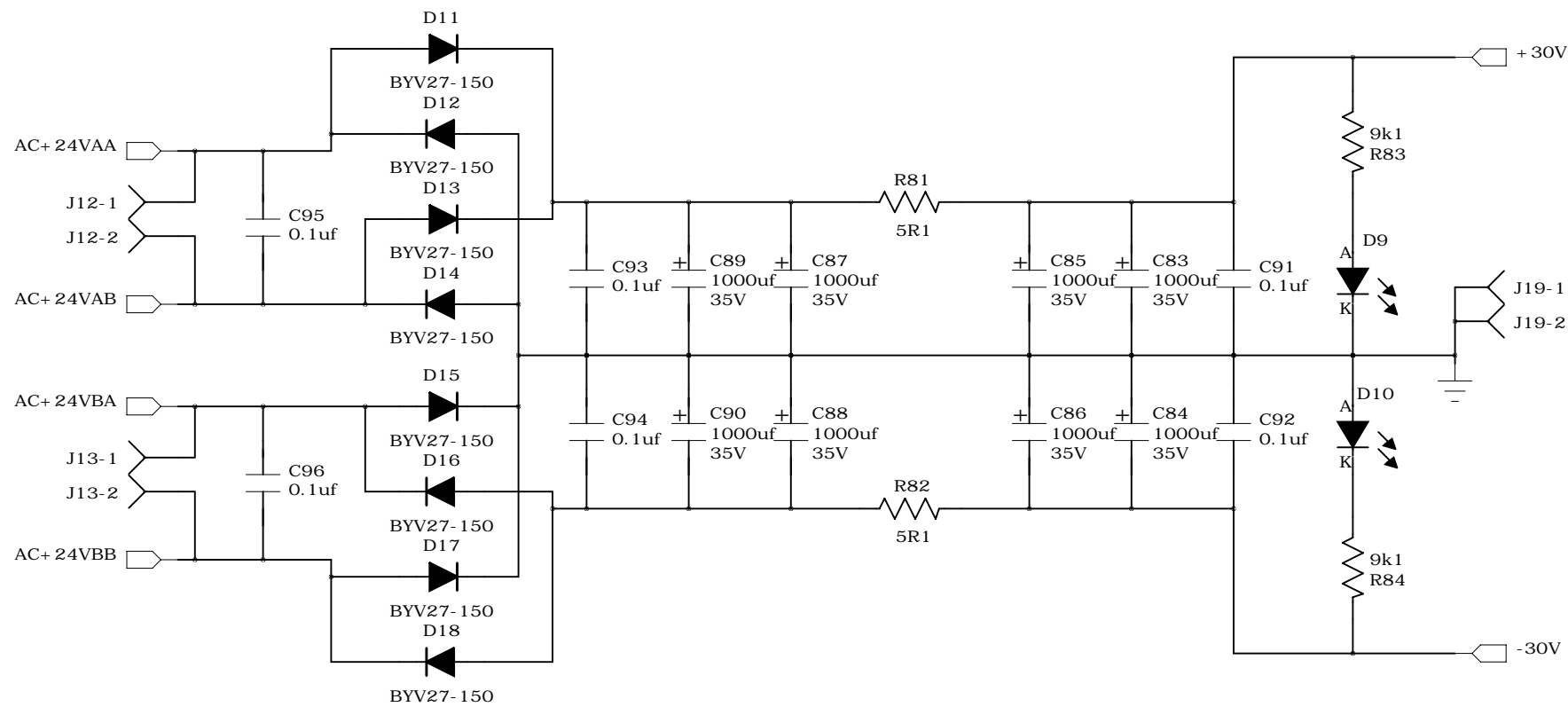
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1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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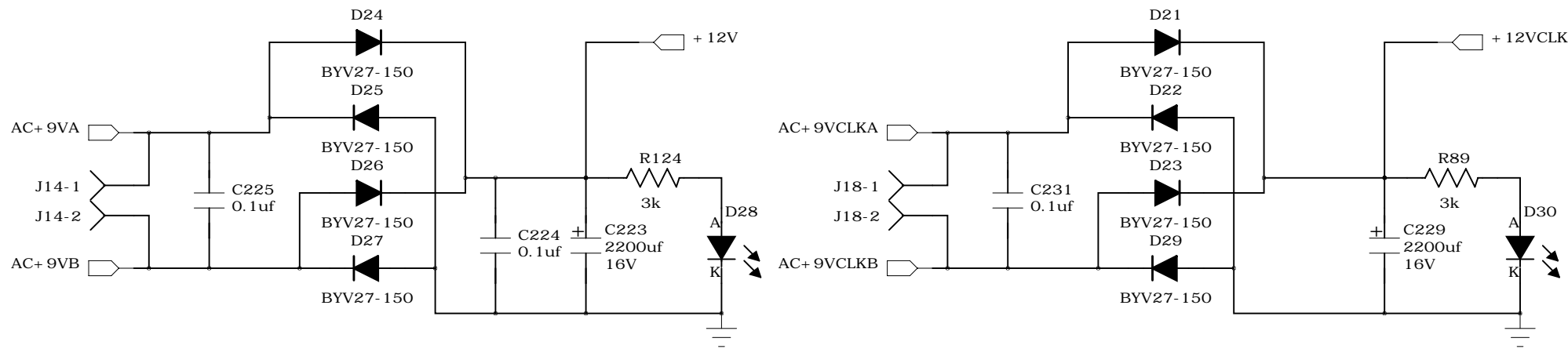


Notes:

- LEDs all use Green Color;; 2.5mm pitch
- All E-cap use Panasoinc FC, 35V 2.5mm & 5mm pitch
- All 0.1uf film cap use Epcos MKP 63V, 5mm pitch
- All resistors use metal film 1%, 1/4W; 12.5mm pitch
- All inductors use > 100mA type; 12.5mm pitch

C

C



B

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COMPANY:			
TITLE: <b>POWER SUPPLY</b>			
DRAWN:	DATED:	CODE:	SIZE:
CHECKED:	DATED:	DRAWING NO:	
QUALITY CONTROL:	DATED:	REV:	
RELEASED:	DATED:	SCALE:	
SHEET: OF			5

6 5 4 3 2 1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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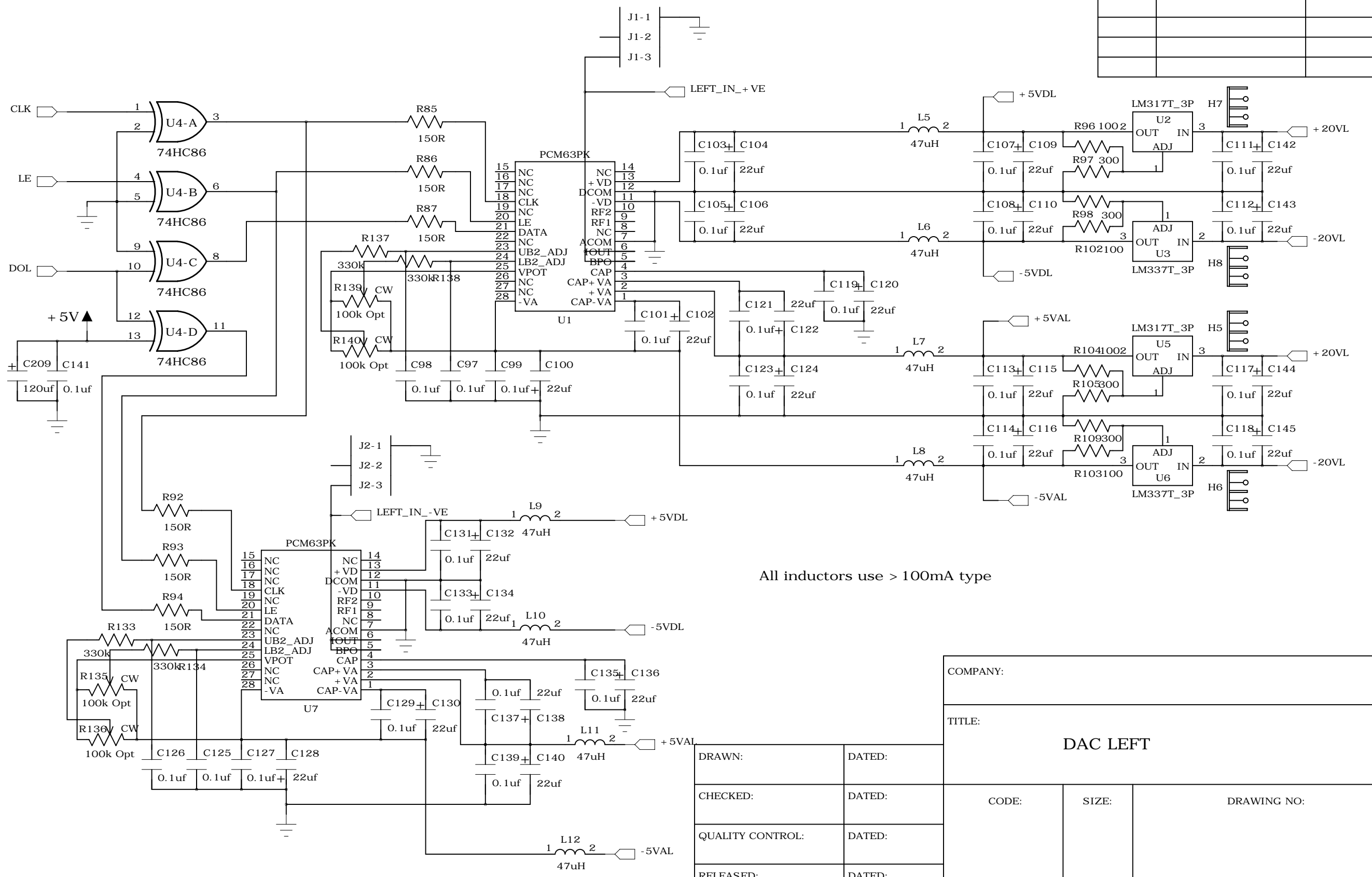
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All inductors use > 100mA type

COMPANY:			
TITLE: <b>DAC LEFT</b>			
DRAWN:	DATED:	CODE:	REV:
CHECKED:	DATED:	SIZE:	DRAWING NO:
QUALITY CONTROL:	DATED:	SHEET: OF 6	
RELEASED:	DATED:	SCALE:	

6 5 4 3 2 1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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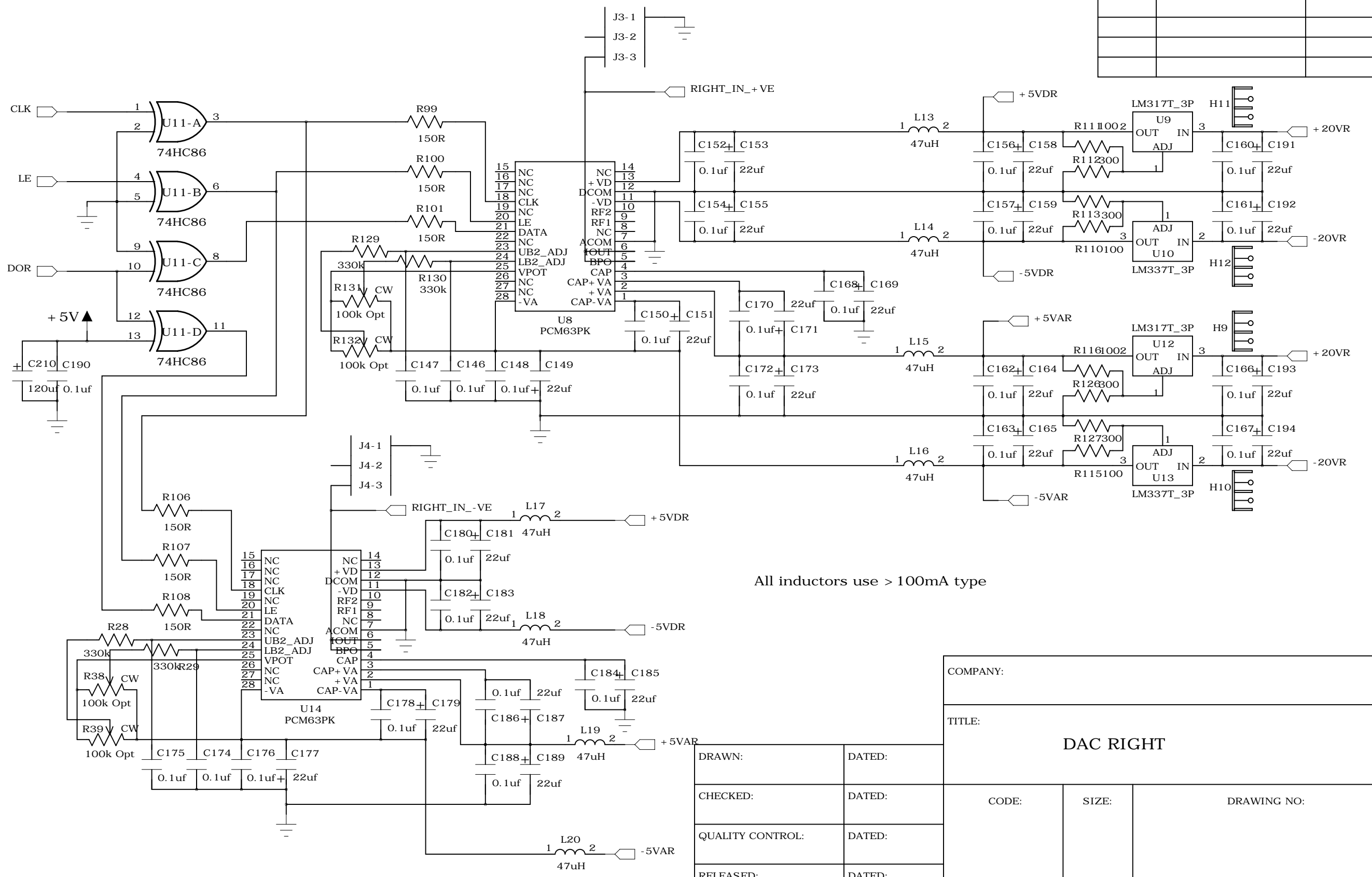
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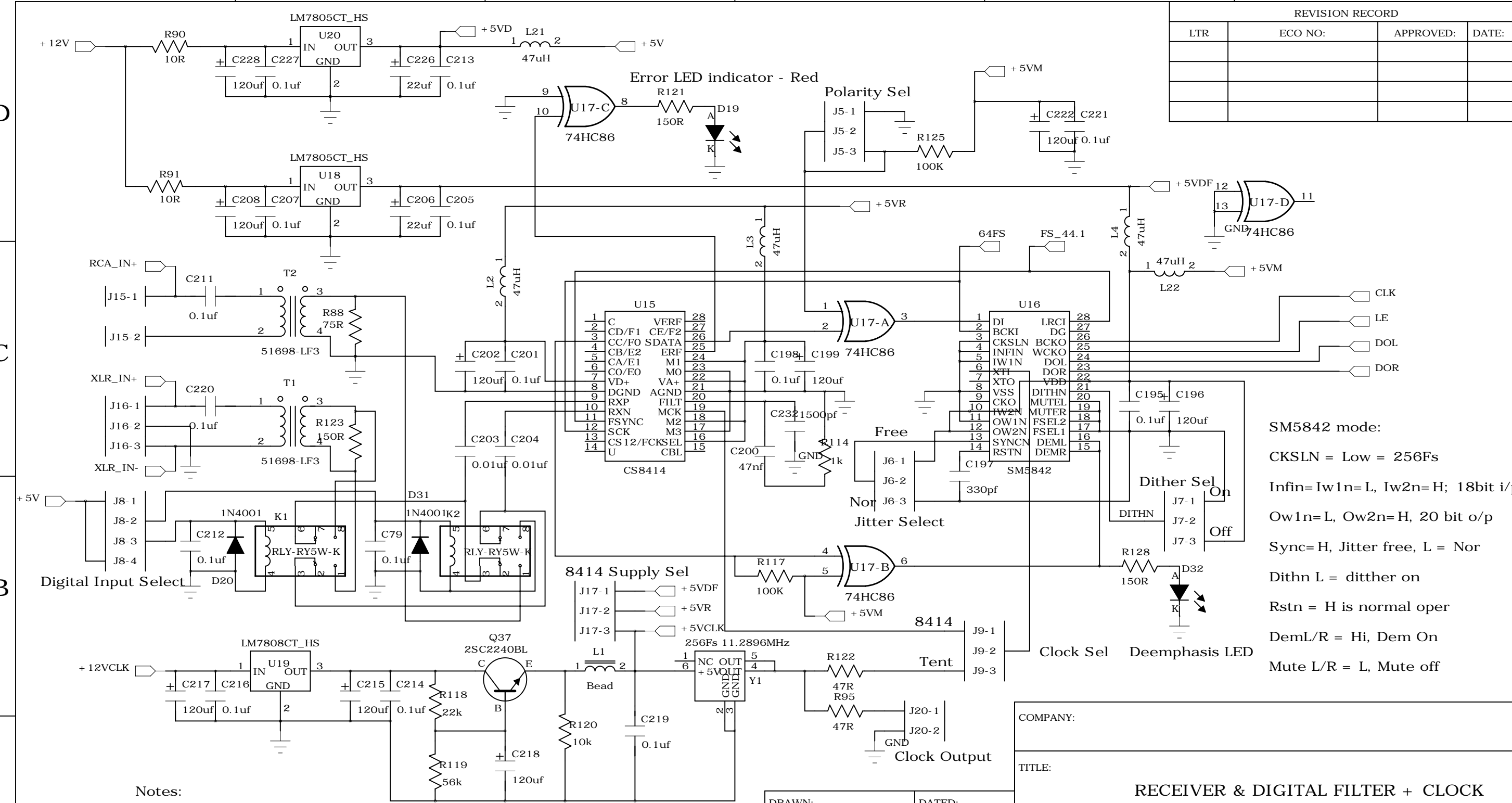


All inductors use > 100mA type

COMPANY:			
TITLE: <b>DAC RIGHT</b>			
DRAWN:	DATED:	CODE:	REV:
CHECKED:	DATED:	SIZE:	DRAWING NO:
QUALITY CONTROL:	DATED:	SHEET: OF 7	
RELEASED:	DATED:	SCALE:	

6 5 4 3 2 1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



SM5842 mode:  
 CKSLN = Low = 256Fs  
 Infin= Iw1n=L, Iw2n=H; 18bit i/p  
 Ow1n=L, Ow2n=H, 20 bit o/p  
 Sync=H, Jitter free, L = Nor  
 Dithn L = dither on  
 Rstn = H is normal oper  
 DemL/R = Hi, Dem On  
 Mute L/R = L, Mute off

Notes:  
 T1 & T2 is Midcom 51698-LF3  
 K1 & K2 are 5V small relay Takamisawa RY5W-K  
 8414 Mode 6, OUt L/R 18 bit, LSBJ

COMPANY:			
TITLE: <b>RECEIVER &amp; DIGITAL FILTER + CLOCK</b>			
DRAWN:	DATED:	CODE:	REV:
CHECKED:	DATED:	SIZE:	DRAWING NO:
QUALITY CONTROL:	DATED:	SHEET: OF 8	
RELEASED:	DATED:	SCALE:	

D

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C

C

B

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