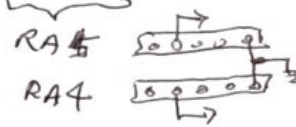


Ym 3436

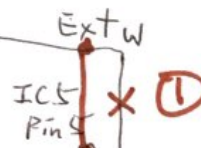
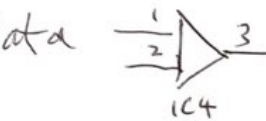
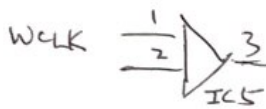
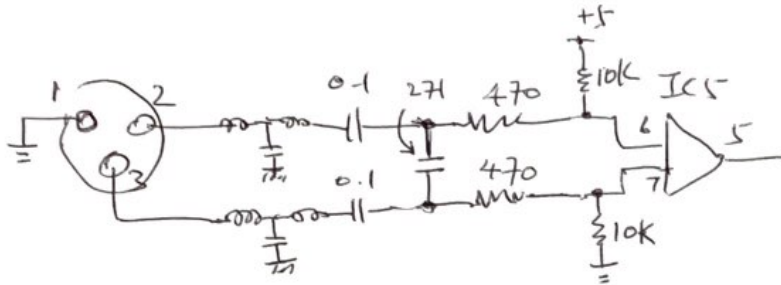
① KMO - pin 32 "L" $\Rightarrow$ DDIN

DDIN pin 37 $\Rightarrow$ ICS - pin 5

ICS - pin 3/4 $\Rightarrow$ AES Yp

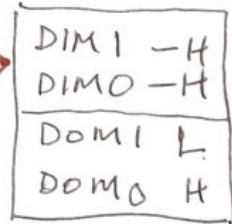
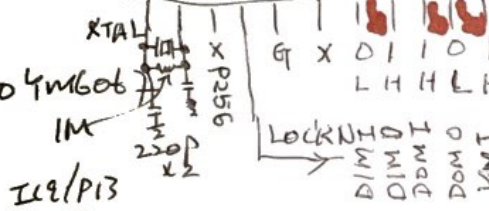
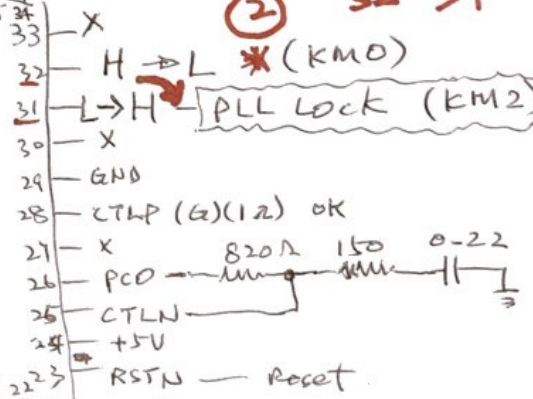
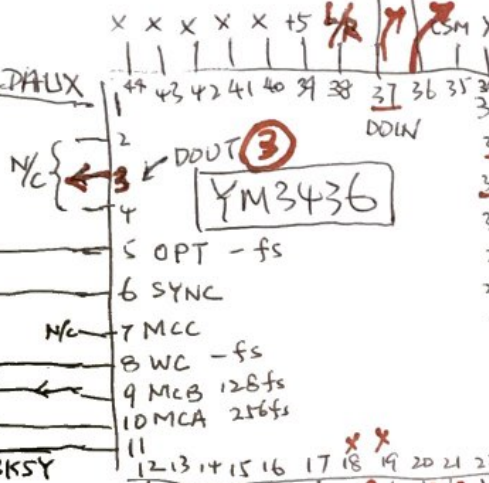
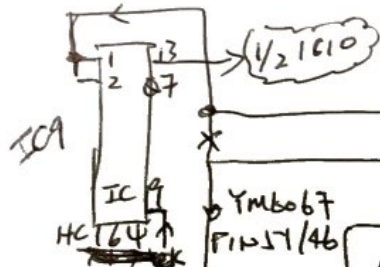


M137118E - ROM



② 32-31

H $\Rightarrow$ L * (KMO)
L $\Rightarrow$ H * PLL Lock (KM2)?



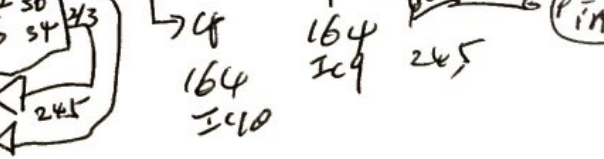
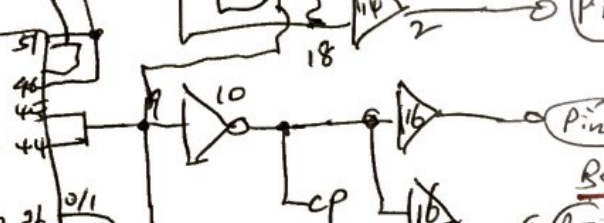
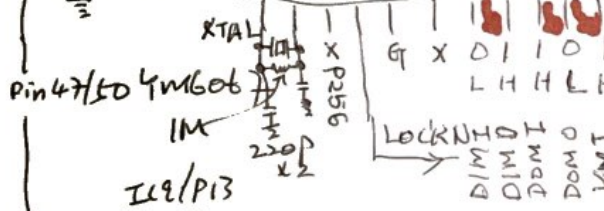
socket #7 x2

XTI MCK

YM 6067 IC6

2/3 DO Pin(2)

0/1 DO Pin(5)



WORD CLK (fs)

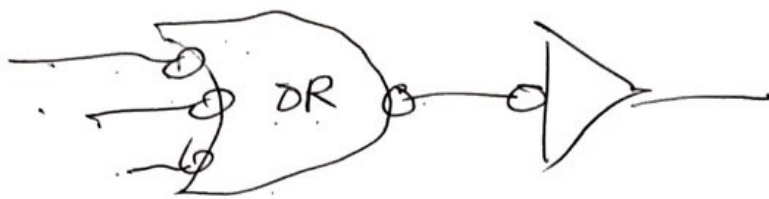
Pin 1 LCK

Pin 1 LCK

Pin 4 RCK

Pin 4 RCK

H



Default
(L)

MS1

MS2 L

Ym606J

MS3 ~~H~~ L

Pin 51/46 SYNO $\rightarrow$ SYNO $\rightarrow$ (6) SYNC

MEN H

Pin 50/47 MCKO/MCK1 $\rightarrow$ (9) 128 fs

Res 44 $\rightarrow$ (25) $\rightarrow$ 3436 23 pin

(8) ?

MCK/ck $\leftarrow$ master clock
ofp

(10) - 256 fs

(15) $\rightarrow$ ~~LOCK~~ ?

18/19 (XTI/XTO)

18/19 0/1

20/21 1/0

22 $\rightarrow$

DIM1	0
DIM0	1
DOM1	1
DOM0	0
KM1	H

31 ☐ KM2

32 H KMO

35 L CSM

36 EXTW - Ext. Workclock $\uparrow$ p

Ym3436

CN501

Socket P1N

50k Δ ✓ (1) ~~LRCI~~ LRCI

NIL (2) DIN - 2/3 CH

(3) GND

3.2 kHz ✓ (4) BCK1

NIL (5) DIN - 3/4 CH

(6) GND

12.8 kHz ✓ (7) XT1 (mclk)

(8) GND

(9) MS1

(10) MS2

(11) MS3

~~(12) MD3~~

✓ (13) $\overline{MEN}$

(L) (14) MUTE (o/p) relay

(H) (15) Emp (Relay)

(H) (16) MUTE (5803)
soft

H C 245 x 2

$\underline{P20/P1} (DIR) = +5V (H)$

$\underline{Pin (19)} = GND = \underline{PM10}$
↳ op enable (Always)

CN502

(1) "Power"

(2) GND

(3) GND

(4) +5V_D

(5) GND

(6) -24V_A

(7) GND

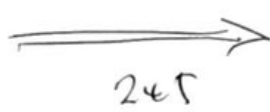
(8) - +24V_A

$DIR = H \Rightarrow$ Input
mode

$A_1 \rightarrow B_1$

$A_n \rightarrow B_n$

Digital I/P



PIN 18 ← LRC1 (28)

245
18

N/A FSC0 (27)

← BCKO (26)

← WKO (25)

PCm 634
← DOL (24)

← DOR (23)

+5V (22)

GND (21)

N/A DGL (20)

N/A DGR (19)

FSEL2 (18)

FSEL1 (17)

DIEM (16)

PMW 11 ← MUTE (15)
245
%P

① DIN }
② BCKI }

③ CKSL = G(L) }
④ CKDV +5V(H) } 245fs

⑤ 8X → G(8X)

⑥ XTI ← MCK

⑦ XTO

⑧ VSS1 → G

⑨ CKO

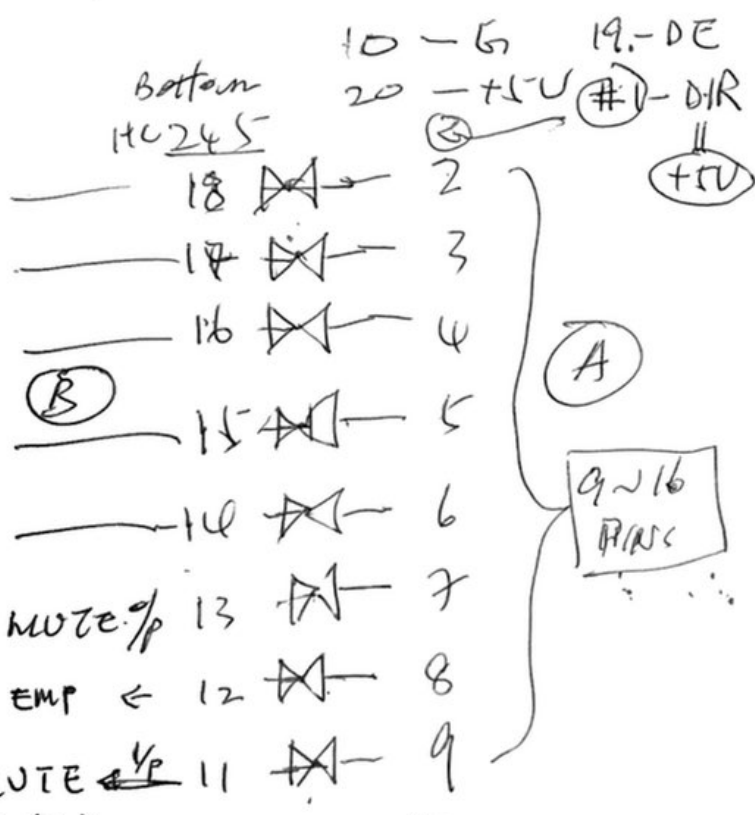
⑩ MS1

⑪ MS2

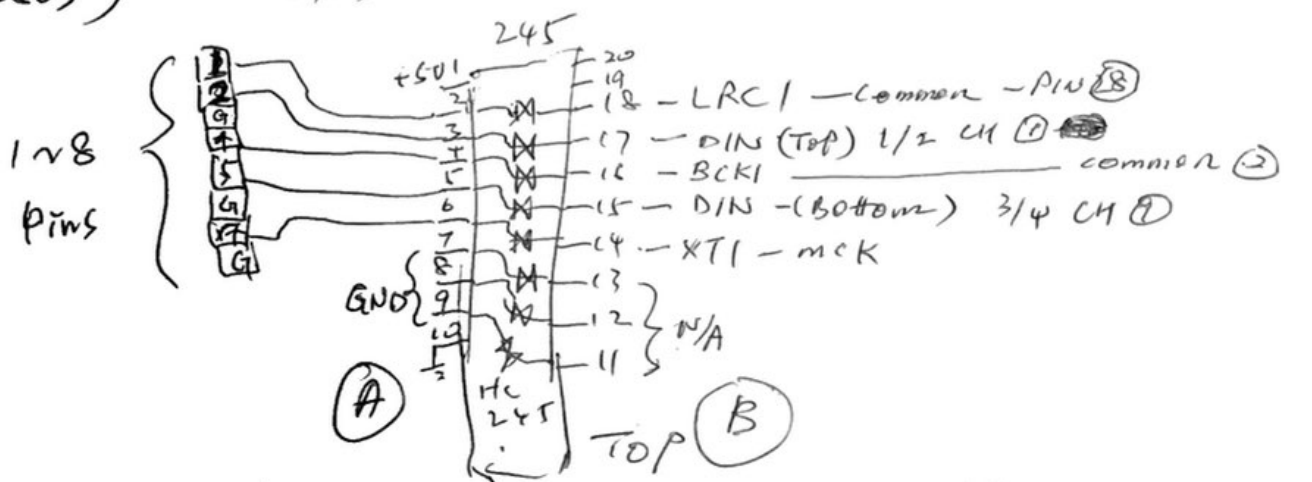
⑫ MS3

⑬ MDT

⑭ MEN



⑮ MUTE ← 11
(①+②)
(5203)



5803 APT

① XTI — $256f_s$

② 8X ^{over} Sampler

③ De-emphasis OFF / $f_s = 44.1 \text{ kHz}$

④ Input: LRCI f_s
DIN1 data
BCKI $64f_s$
XTI — $256f_s$

} $\Rightarrow$ DI

⑤ o/p mode: DOR
DOL
WCKO
BCKO

} $\Rightarrow$ PCM63